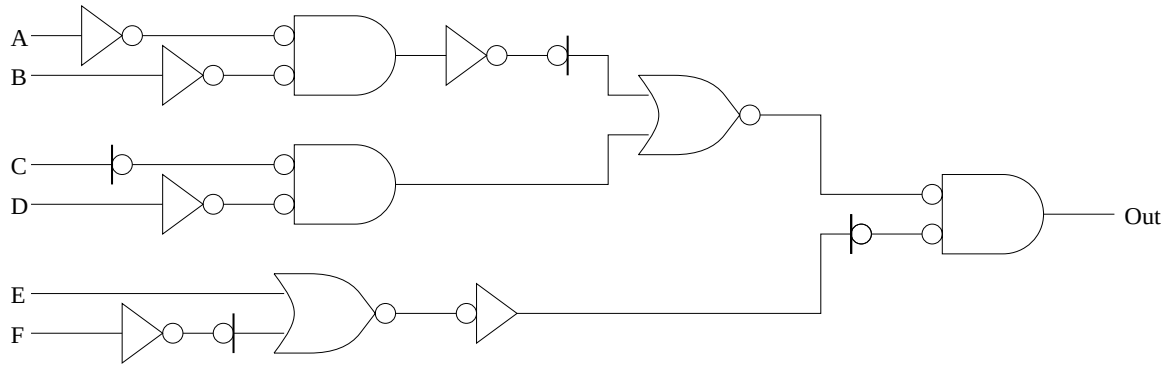
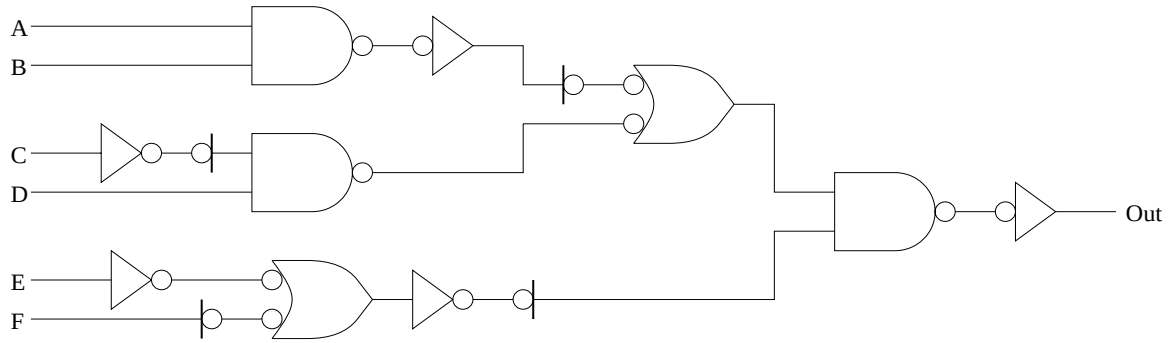
Combinational Design

Part A



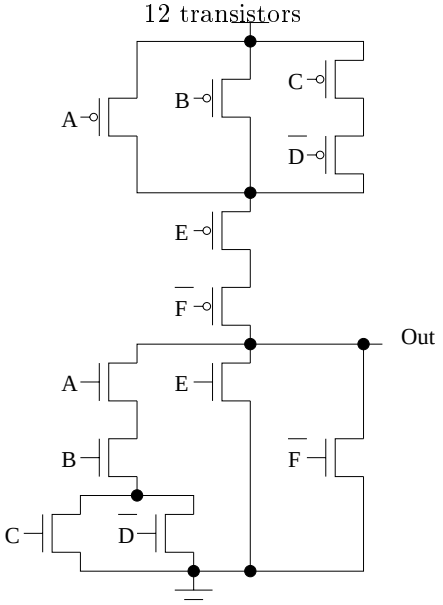
32 transistors

Part B



30 transistors

Part C $Out = (\overline{A} + \overline{B} + \overline{C}\,D)\,\overline{E}\,F$

Part D

12 transistors